

Application Data Sheet

APPLICATION INFORMATION

Application Number::

Filing Date::

Application Type:: Regular

Subject Matter:: Utility

Suggested classification::

Suggested Group Art Unit::

CD-ROM or CD-R?:: None

Number of CD Disks:

Number of Copies of CDs::

Sequence Submission?::

Computer Readable From (CRF)?:: No

Number of Copies of CRF::

Title:: LOGIC SIMULATION APPARATUS FOR
PERFORMING LOGIC SIMULATION IN HIGH
SPEED

Attorney Docket Number:: 402855

Request for Early Publication?:: No

Request for Non-Publication?:: No

Suggested Drawing Figure::

Total Drawing Sheets:: 5

Small Entity?:: No

Latin Name::

Variety denomination name::

Petition Included?:: No

Petition Type::

Licensed US Govt. Agency::

Contract or Grant Numbers::

Secrecy Order in Parent Appl.?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Country:: Japan
Status:: Full Capacity
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Middle Name::
Family Name:: YOSHIDA
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4-1, Marunouchi 2-chome, Chiyoda-ku
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Inventor Authority Type:: Inventor
Primary Citizenship Country:: Japan
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Middle Name::
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CORRESPONDENCE INFORMATION

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REPRESENTATIVE INFORMATION

Representative Customer Number:: 23548

Representative Designation:: Registration Number:: Representative Name::

DOMESTIC PRIORITY INFORMATION

Application:: Continuity Type:: Parent Application:: Parent Filing Date::

FOREIGN APPLICATION INFORMATION

| | | | |
|-----------|----------------------|---------------|------------------|
| Country:: | Application Number:: | Filing Date:: | Priority Claimed |
| Japan | 2002-350118 | 12/02/02 | Yes |

ASSIGNEE INFORMATION

| | |
|--|---------------------------------------|
| Assignee name:: | Renesas Technology Corp. |
| Street of mailing address:: | 4-1, Marunouchi 2-chome Chiyoda-ku |
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